



INFORMATION DISCLOSURE CITATION
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APPLICANT(S):

PAVEL HORSKY ET AL.

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GROUP ART UNIT:

2819

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JBJ6	A.	2002/113723	08-22-2002	Greig David Vetea	—	—	
	B.	6,160,507	12-12-2000	Carbou Pierre et al.	—	—	
	C.	5,327,134	07-05-1994	Nakamura Yasuyuki et al.	—	—	
	D.	5,600,319	02-04-1997	Ginetti Bernard	—	—	
	E.	5,453,743	09-26-1995	Kang Ku-Chang	—	—	
JBJ6	F.	5,254,994	10-19-1993	Goto Junkei et al.	—	—	
	G.						
	H.						
	I.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
JBJ6	J.	EP 0 930 717	07-21-1999	EP	—	—	X	
	K.							
	L.							
	M.							
	N.							
	O.							
	P.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

JBJ6	Q.	Y. Nakamura et al., "A 10-bit 70 MS/s CMOS D/A Converter," IEEE 1990 Symposium on VLSI Circuits, June 7, 1990, pages 57-58.						
	R.							
	S.							
	T.							
	U.							
	V.							
	W.							

EXAMINER

Alan Bruner Jeanblanc

DATE CONSIDERED

3/22/05

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